

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

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1. (Currently Amended) A display device, comprising:

a plurality of write lines;

a plurality of data lines;

a plurality of dots disposed correspondingly to intersections between the plurality of write lines and the plurality of data lines, each of the plurality of dots that is a minimum unit for displaying, including:

a storing section that stores a data signal to control display, the storing section having a memory cells configured by a static circuits and holding binary signals, and a number of the memory cells corresponding to a tonal level, a degree of the tonal level being determined by the data signal stored in the memory cells; and

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a display control section that performs display control on the basis of the digital data signal held by the storing section.

2. (Previously Presented) A display device, comprising:

a plurality of write lines;

a plurality of data lines;

a plurality of dots disposed correspondingly to intersections between the plurality of write lines and the plurality of data lines, each of the plurality of dots that is a minimum unit for displaying, including:

a storing section storing a data signal when a write signal is transmitted through a respective one of the plurality of write lines;

a converting section that converts a value based on a value of the data signal held by the storing section into an analog signal; and

a display control section that performs tonal control on the basis of the analog signal converted by the converting section.

3. (Previously Presented) The display device according to claim 2, the at least one storing circuit including a memory cell configured by a static circuit.

4. (Previously Presented) The display device according to claim 2, the analog signal being represented as a PWM waveform generated by the converting section.

5. (Previously Presented) The display device according to claim 2, the converting section converting the data signal to the analog signal including γ -characteristic.

6. The display device as claimed in claim 2, said converting section performing conversion into the analog signal at a constant period interval.

7. (Previously Presented) The display device as claimed in claim 6, a duration that no conversion into the analog signal is made being provided in the constant period.

8. (Previously Presented) The display device as claimed in claim 7, each converting section being different in a start time of the constant period, and the period and the duration no conversion into the analog signal is made being different.

9. (Previously Presented) The display device as claimed in claim 6, an alternating current drive voltage corresponding to the constant period being applied to said display control section.

10. (Previously Presented) The display device as claimed in claim 9, the alternating current drive voltage being a voltage driven at $V_{COM} \pm V_a$ with respect to a reference voltage V_{COM} .

11. (Previously Presented) The display device as claimed in claim 9, the alternating current drive voltage being a voltage alternating-current-inversion-driven by two voltage-applying lines laid correspondingly to said dot array pattern.

12. (Previously Presented) The display device as claimed in claim 9, a plurality of rows of said dot array being provided by groups, and rows in pair being set in each of the groups to invert a phase of the alternating current drive voltage applied.

13. (Previously Presented) The display device as claimed in claim 2, said display control section controlling light emission of current-driven luminescent devices in connection on the basis of the analog signal in place of performing tonal control using a liquid crystal, thereby effecting tonal control.

14. (Currently Amended) A display device, comprising:

a plurality of write lines;

a plurality of data lines;

a plurality of dots disposed correspondingly to intersections between the plurality of write lines and the plurality of data lines, each of the plurality of dots that is a minimum unit for displaying, including:

a storing section having ~~at least one~~ memory cells configured by a static circuit and holding binary signals, and a number of the memory cells corresponding to a tonal level, a degree of the tonal level being determined by the data signal stored in the memory cells, the storing section storing a data signal when a write signal is transmitted through a respective one of the plurality of write lines;

a luminescent section including a plurality of luminescent elements having different areas; and

an active device section connected to the storing section and the luminescent section.

15. (Previously Presented) The display device according to claim 14, the plurality of luminescent elements being EL elements.

16. (Previously Presented) The display device according to 14, the plurality of luminescent elements being organic EL elements.

17. (Currently Amended) A display device, comprising:

a plurality of write lines;

a plurality of data lines; and

a plurality of dots disposed correspondingly to intersections between the plurality of write lines and data lines, each of the plurality of dots being a minimum unit of displaying, and including:

a storing section having ~~at least one memory cell~~ cells configured by a static circuit and holding binary signals, and a number of the memory cells corresponding to a tonal level, a degree of the tonal level being determined by the data signal stored in the memory cells, the storing section storing a data signal when a write signal is transmitted through a respective one of the plurality of write lines; and

an active device section connected to the storing section.

18. (Previously Presented) The display device according to claim 1, further including a plurality of read lines disposed correspondingly to the plurality of dots, read out of the data signal held by the storing section being performed when a read signal is transmitted through a respective one of the plurality of read lines.

19. (Previously Presented) A display device, comprising:

a plurality of write lines;

a plurality of data lines;

an active-matrix section having a plurality of dots disposed correspondingly to intersections of the plurality of write lines and the plurality of data lines, each of the plurality of dots including a storing section having a memory cell configured by a static memory, the storing section storing a data signal supplied through a respective one of the plurality of data

lines, each of the plurality of dots including and electro-optical conversion section that performs an electro-optical conversion on the basis of the data signal held by the storing section;

a column decoder section that selects a data line of the plurality of data lines;
and

a row decoder section that selects a row of the plurality of write lines through which a write signal is transmitted, the write signal being supplied to only a dot to be written of the plurality of dots.

20. (Previously Presented) The display device according to claim 19, each of the plurality of dots further including a converting section that converts a value based on a value of the data signal held by the storing section into an analog signal.

21. (Previously Presented) The display device according to claim 19, further comprising a plurality of first lines for supplying a voltage as a power source to the storing section, the plurality of first lines being shared between two rows of the plurality of write signal lines.

22. (Previously Presented) The display device according to claim 19, the row decoder section being allocated correspondingly to a length the active-matrix section in a column direction, and the column decoder section being allocated correspondingly to a length of the active-matrix section in a row direction.

23. (Previously Presented) The display device according to claim 19, further including a column selection switch section that transmits the data signal to a data line of the plurality of data lines selected by the column decoder section.

24. (Previously Presented) The display device according to claim 19, the row decoder that selects a row of the plurality of write lines through which a write signal is transmitted on the basis of an address signal.

25. (Previously Presented) The display device according to claim 19, the column decoder section that selects a data line of the plurality of data lines on the basis of an address signal.

26. (Previously Presented) The display device according to claim 19, further including:

a plurality of pixels each of which being provided by three dots for red, green and blue, respectively, of the plurality of dots;

the column decoder section selecting data lines of the plurality of data lines corresponding to a respective pixel of the plurality of pixels; and

the data signal being supplied together to the three dots included in a respective one of the plurality of pixels.

27. (Previously Presented) The display device according to claim 19, further including:

a plurality of pixels each of which being provided by three dots for red, green and blue, respectively, of the plurality of dots;

the column decoder section selecting data lines of the plurality of data lines corresponding to respective pixels of the plurality of pixels; and

the data signal being supplied together to the three dots included in the respective pixels.

28. (Previously Presented) A display device, comprising:

a substrate;

a plurality of write lines;

a plurality of data lines;

an active-matrix section having a plurality of dots disposed correspondingly to intersections of the plurality of write lines and the plurality of data lines, each of the plurality

of dots including an electro-optical conversion section that performs an electro-optical conversion on basis of a data signal supplied through a respective one of the plurality of data lines;

a column decoder section that selects a data line of the plurality of data lines;

and

a row decoder section that selects a row of the plurality of write lines through which a write signal is transmitted;

a timing controller section that controls at least timing of transmitting an address signal on the basis of which at least one of the column decoder section and the decoder section selects at least one data line of the plurality of data lines and the row of the plurality of write lines; and

the plurality of write lines, the plurality of data lines, the active-matrix section, the column decoder section, the row decoder section, and the timing controller section being integrally formed on the substrate.

29. (Previously Presented) A display device, comprising:

a substrate;

a plurality of write lines;

a plurality of data lines;

an active-matrix section having a plurality of dots disposed correspondingly to intersections of the plurality of write lines and the plurality of data lines, each of the plurality of dots including an electro-optical conversion section that performs an electro-optical conversion on basis of a data signal supplied through a respective one of the plurality of data lines;

a column decoder section that selects a data line of the plurality of data lines;

a row decoder section that selects a row of the plurality of write lines through which a write signal is transmitted; and

a memory controller section that controls transmission of the data signals, the plurality of write lines, the plurality of data lines, the active-matrix section, the column decoder section, the row decoder section, and the memory controller section being integrally formed on the substrate.

30. (Previously Presented) The display device according to claim 23, the column selection switch section being allocated correspondingly to a length of the active-matrix section in a row direction.

31. (Previously Presented) The display device according to claim 19, further comprising a plurality of second lines for supplying a signal that controls the electro-optical conversion section to the electro-optical conversion section, the plurality of second lines being shared between two rows of the plurality of write signal lines.

32. (Previously Presented) The display device according to claim 1, each of the plurality of dots further comprising an electro-optical conversion section that performs an electro-optical conversion on the basis of a data signal supplied through a respective one of the plurality of data lines.

33. (Previously Presented) The display device according to claim 32, the electro-optical conversion section being a luminescent section.

34. (Previously Presented) A display device, comprising:
 a plurality of write lines;
 a plurality of data lines;
 an active-matrix section having a plurality of dots disposed correspondingly to intersections of the plurality of write lines and the plurality of data lines, each of the plurality of dots including:

a storing section having a plurality of memory cells each of which is configured by a static memory, the storing section storing a data signal supplied through a respective one of the plurality of data lines; and

an electro-optical conversion section that performs an electro-optical conversion on the basis of the data signal held by the storing section, the electro-optical conversion section including a plurality of electro-optical elements.

35. (Previously Presented) The display device according to claim 34, the electro-optical conversion section being any one of an liquid crystal section and luminescent section.